

FIG. 1a

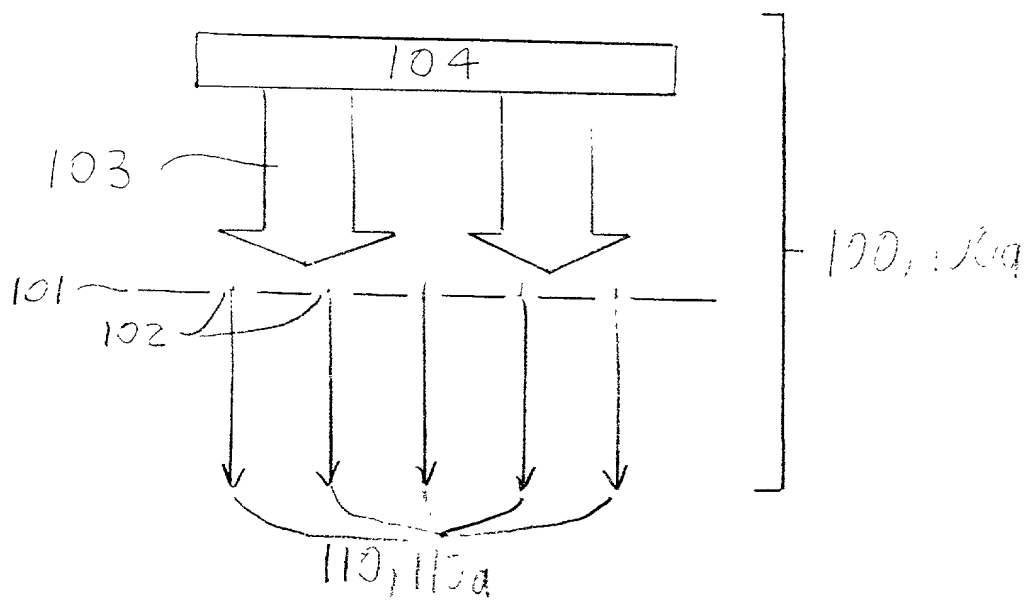


FIG. 1b

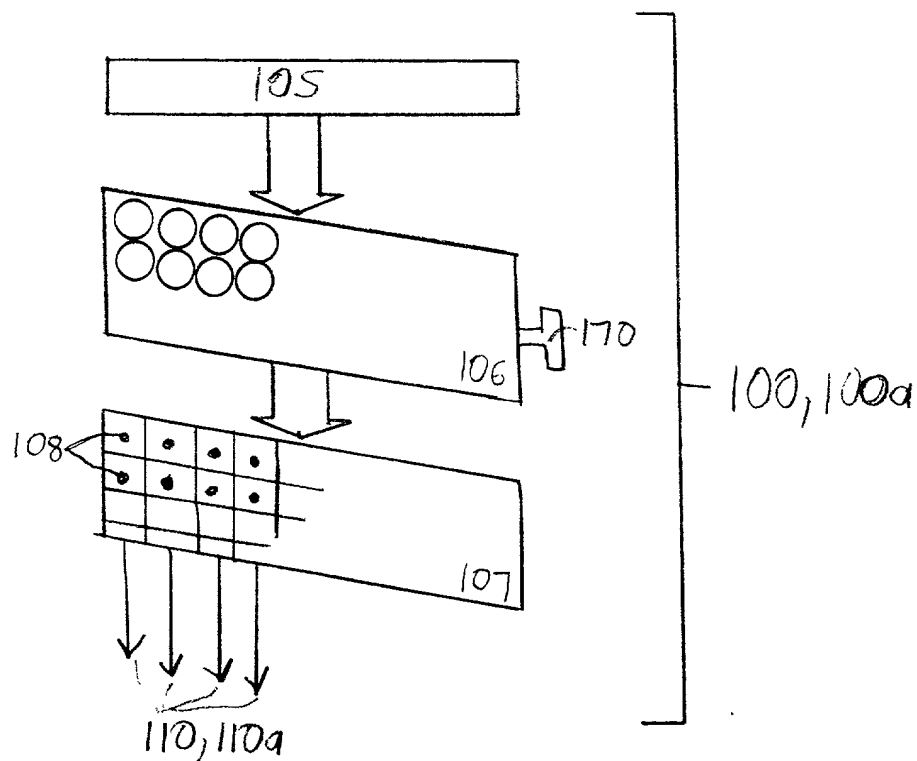


FIG. 1c

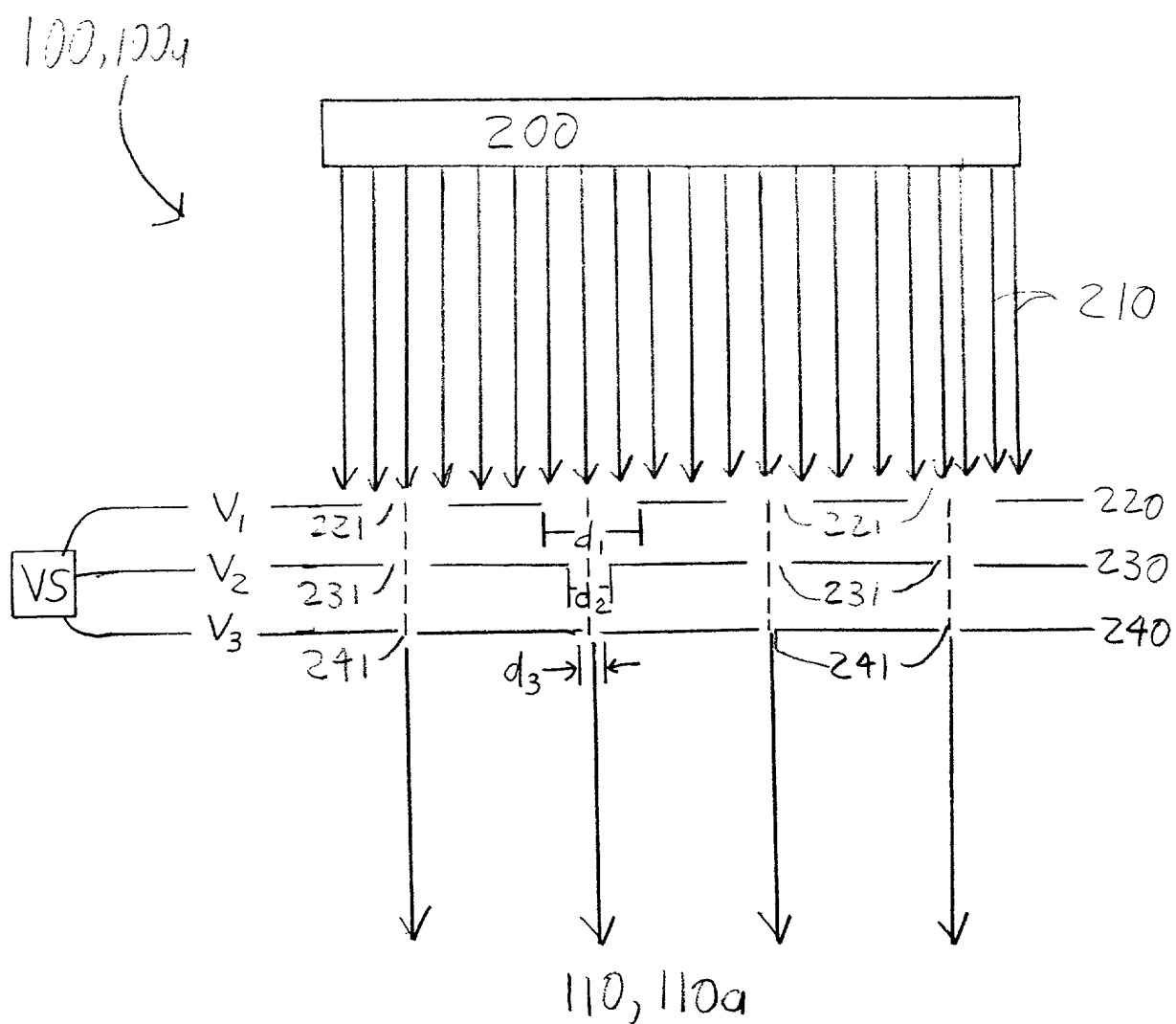


FIG. 1a

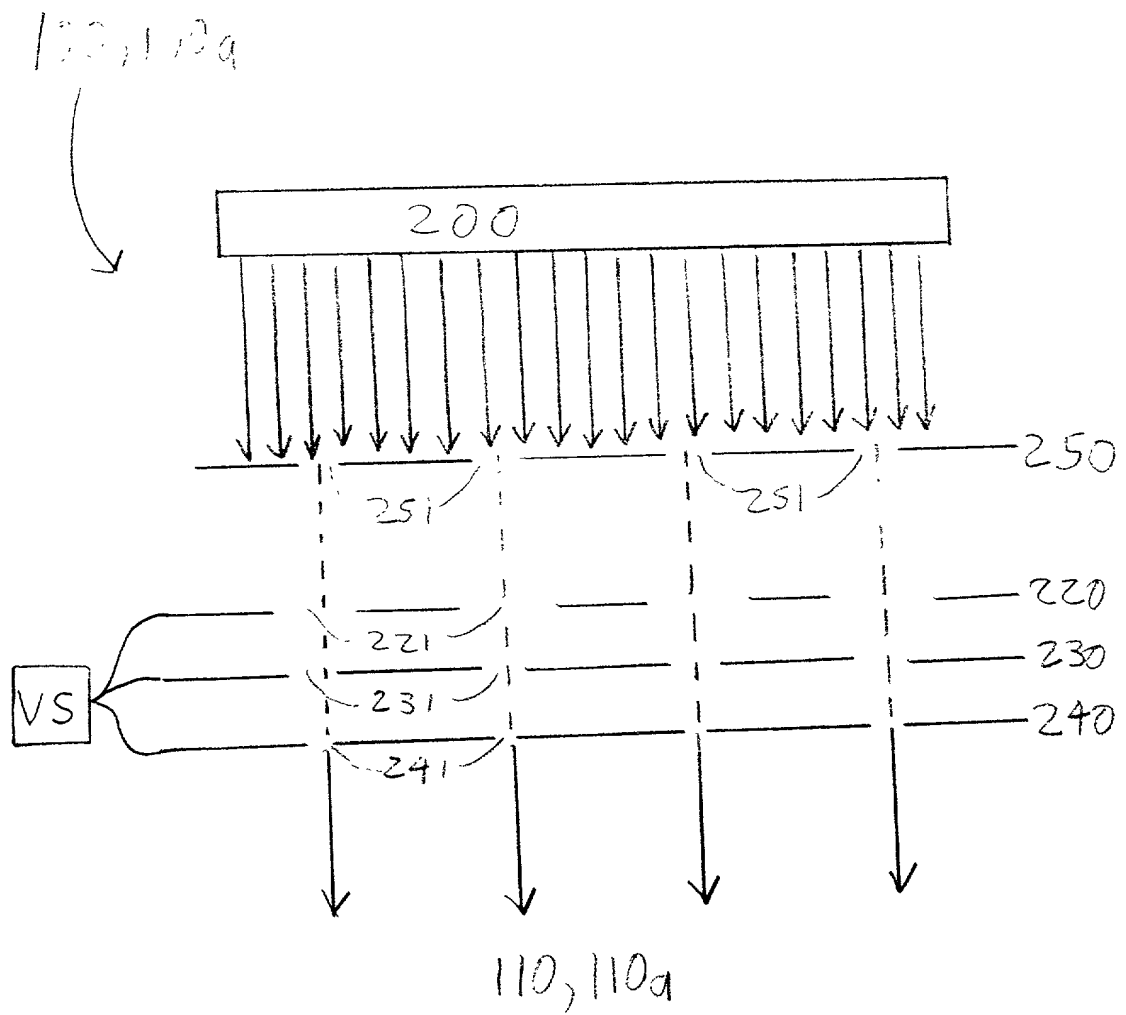
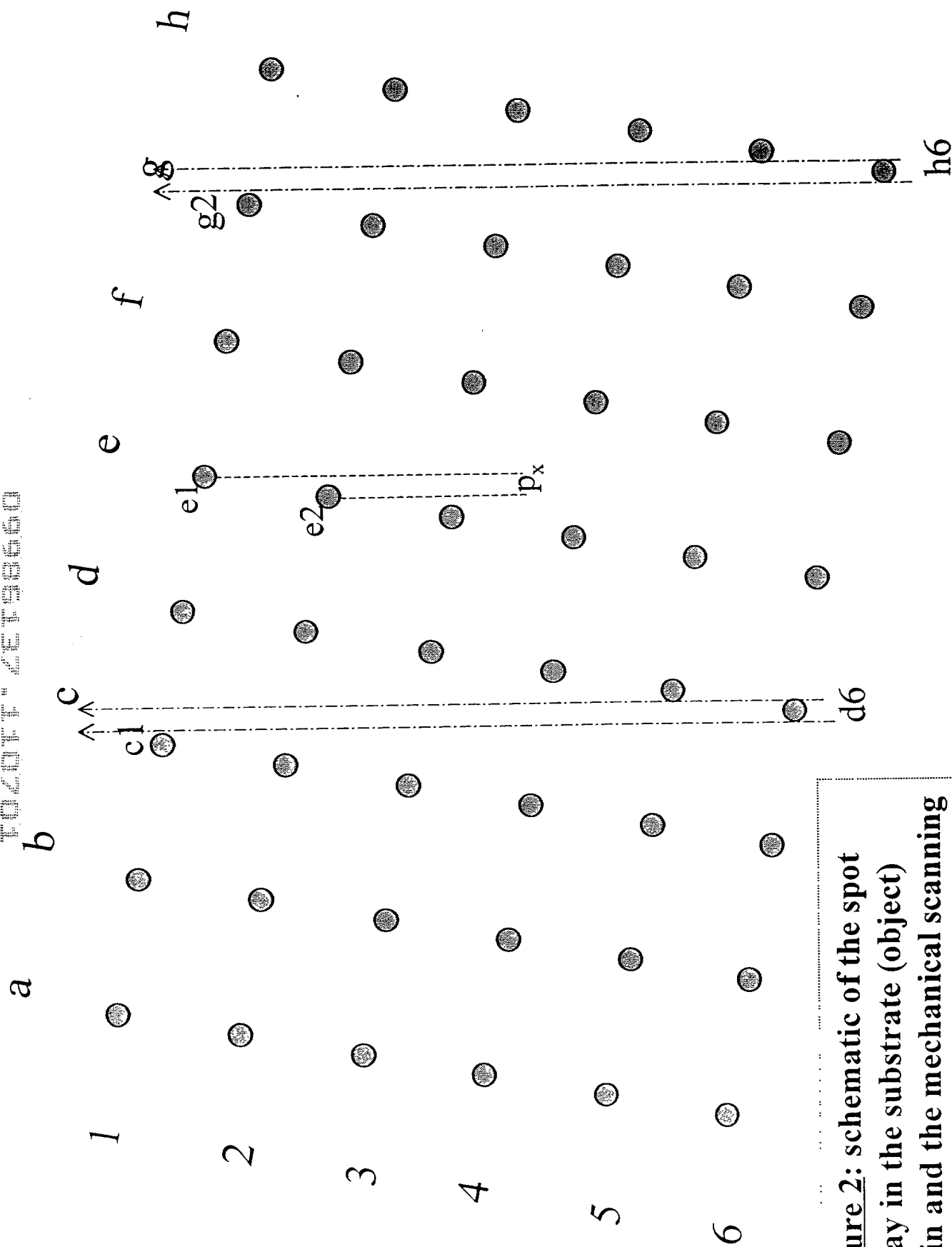


FIG. 1e



**Figure 2: schematic of the spot array in the substrate (object) plain and the mechanical scanning mechanism**

Fig, 3a

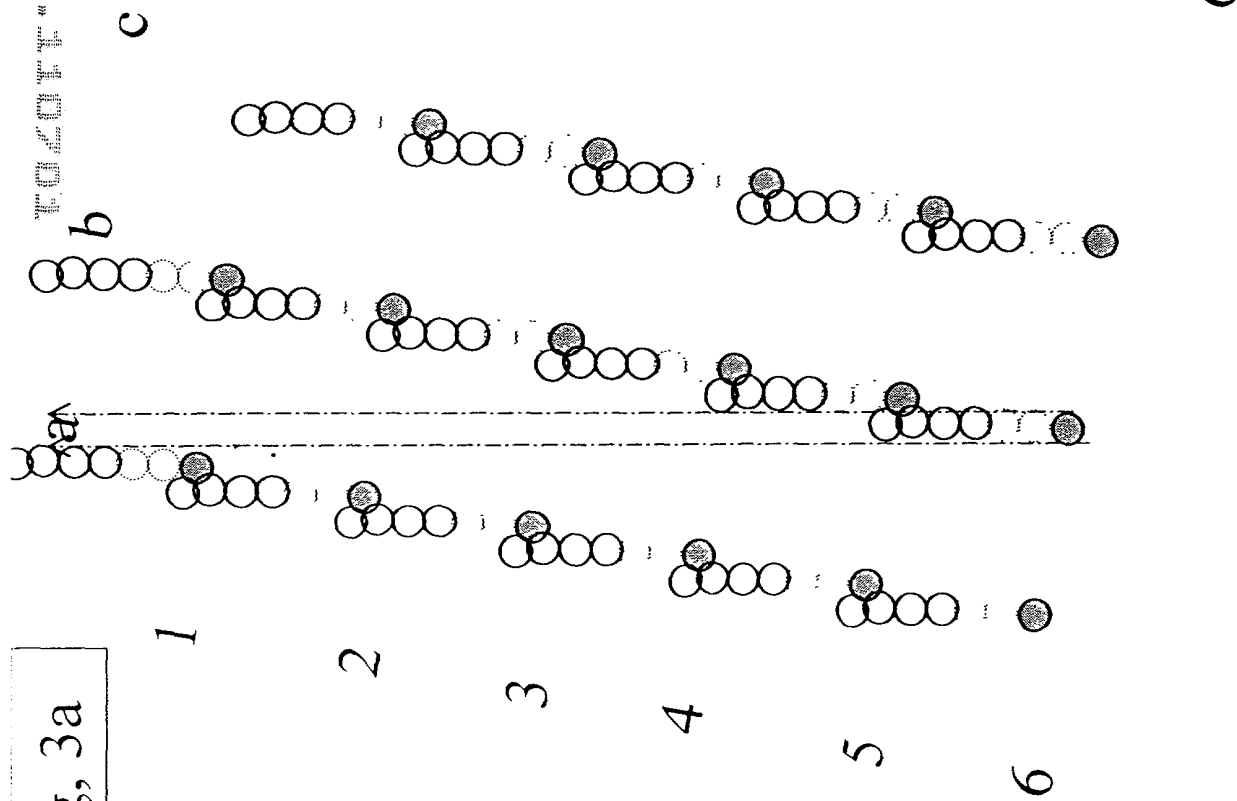


FIG. 3a

Fig, 3b

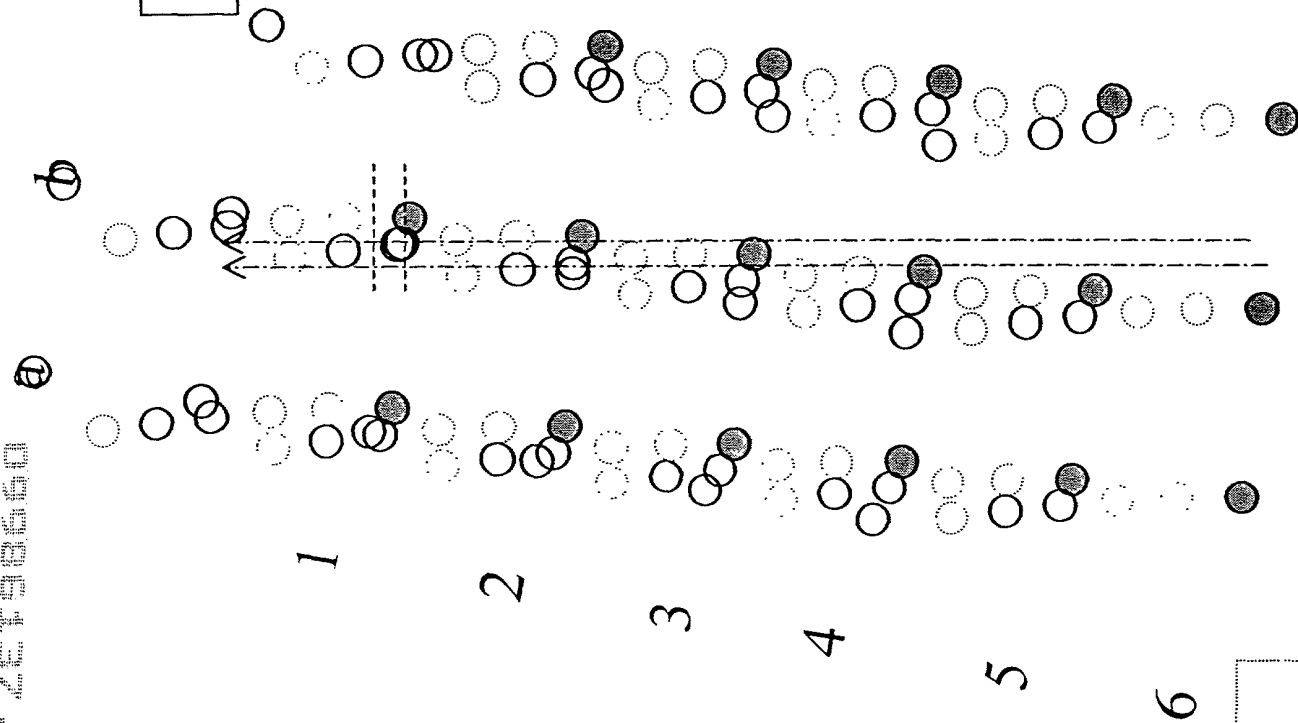
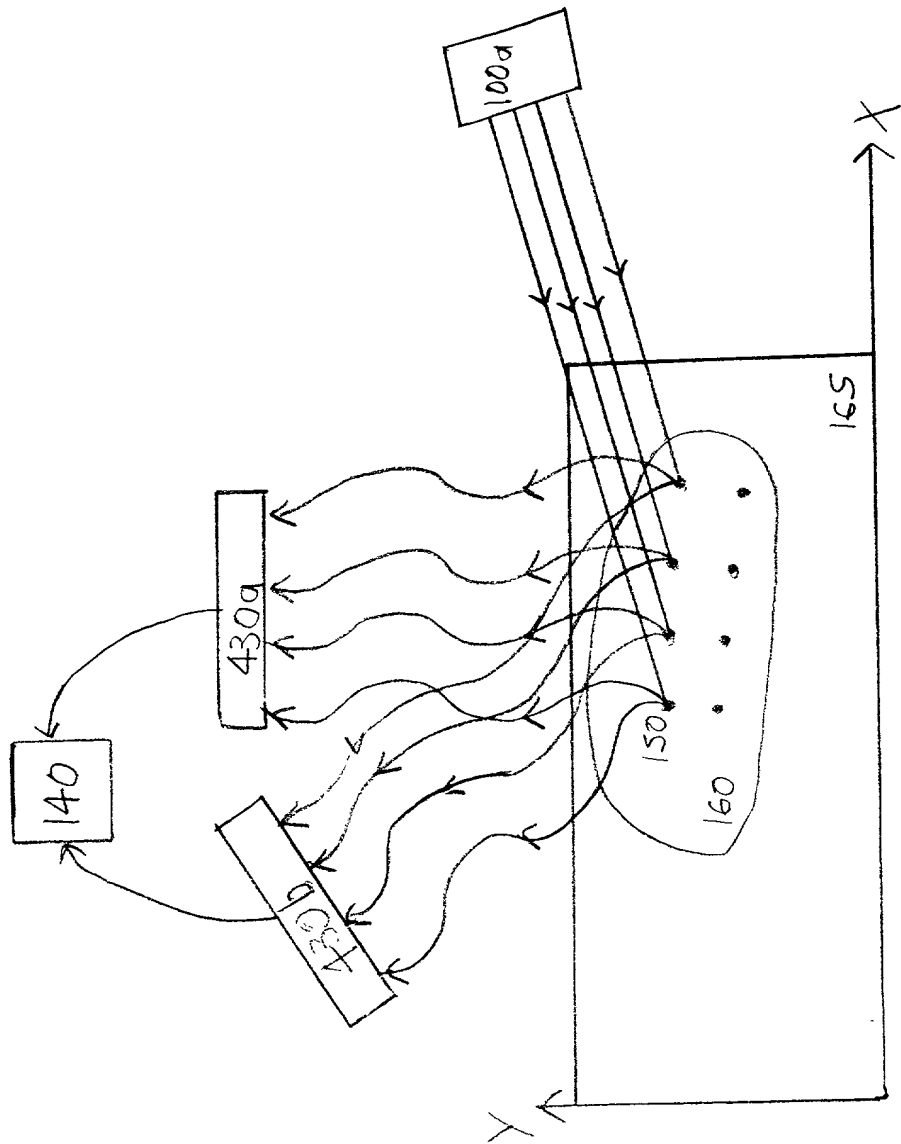


Figure 3a: schematic of a spot array in the standard (a) and interlaced (b) embodiment

FIG. 4



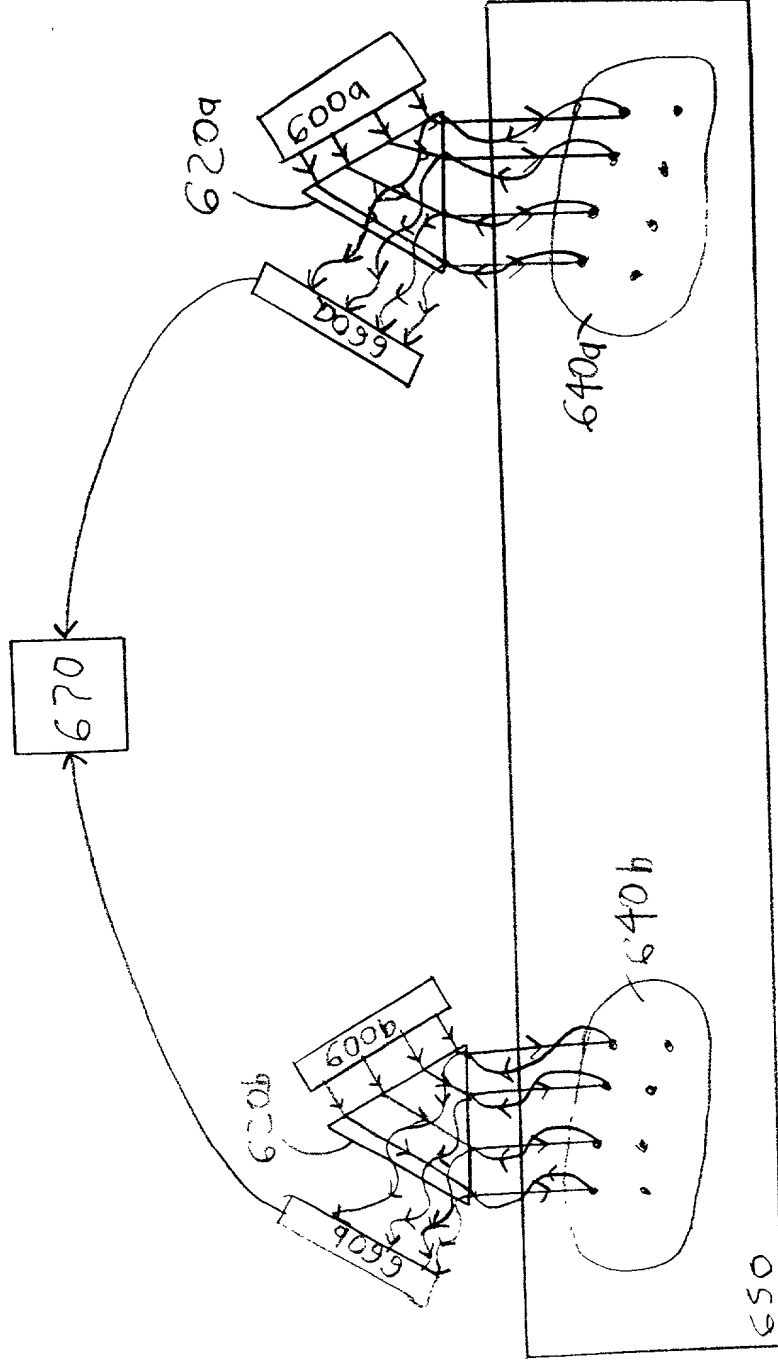


FIG. 5